

ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention	SEMICONDUCTOR EMBEDDED MEMORY DEVICES HAVING BIST CIRCUIT SITUATED UNDER THE BONDING PADS																																																																																																																																				
<p>Submission Type : Utility Patent Filing</p> <p>Application Number: 10/708762 </p> <p>EFS ID: 57705</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 30%;">Confirmation Code</th> <th style="width: 70%;">Message</th> </tr> </thead> <tbody> <tr> <td style="padding: 5px;">ISVR1</td> <td style="padding: 5px;">Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application</td> </tr> <tr> <td style="padding: 5px;">ICON1</td> <td style="padding: 5px;">2761</td> </tr> </tbody> </table> <p>First Named Applicant: Ming-Jing HO</p> <p>Attorney Docket Number: NAUP0567USA</p> <p>Timestamp: 2004-03-24 03:22:04 EDT</p> <p>From: US</p> <p>File Listing:</p> <table border="1" style="width: 100%; border-collapse: collapse; font-size: small;"> <thead> <tr> <th style="width: 30%;">Doc. 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